

#3

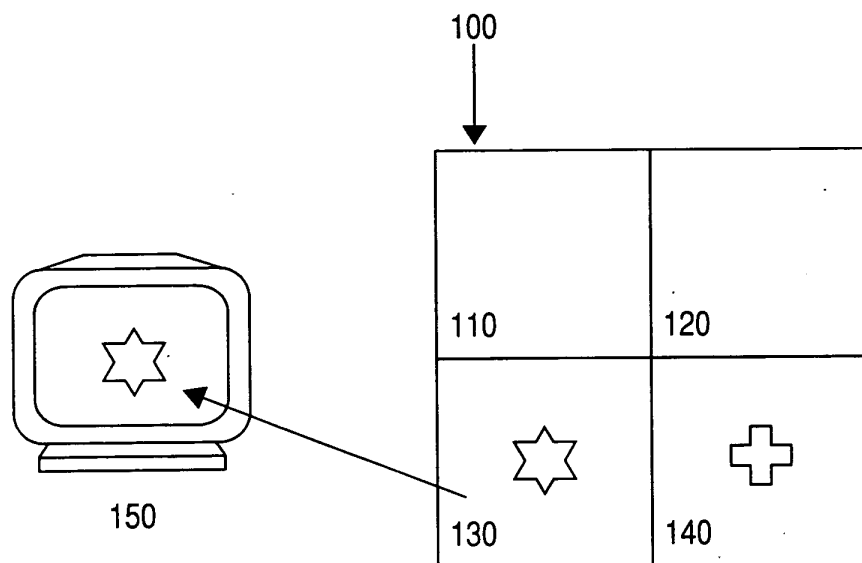


FIGURE 1

09733579-040201

09733579.04001
T022040" 6/5EE260

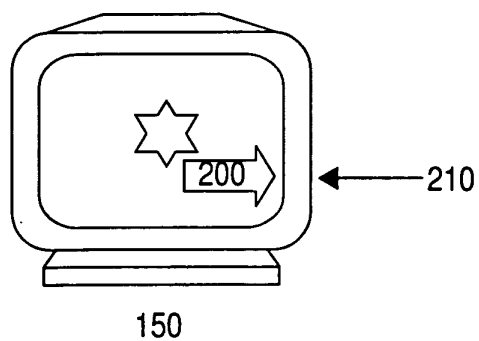


FIGURE 2

09733579-040201
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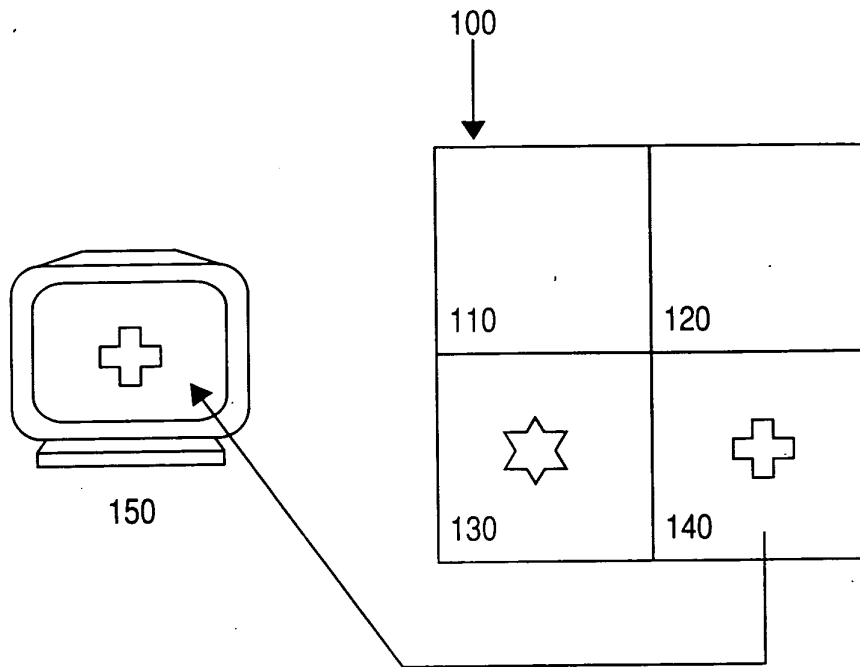


FIGURE 3

09733579.040201

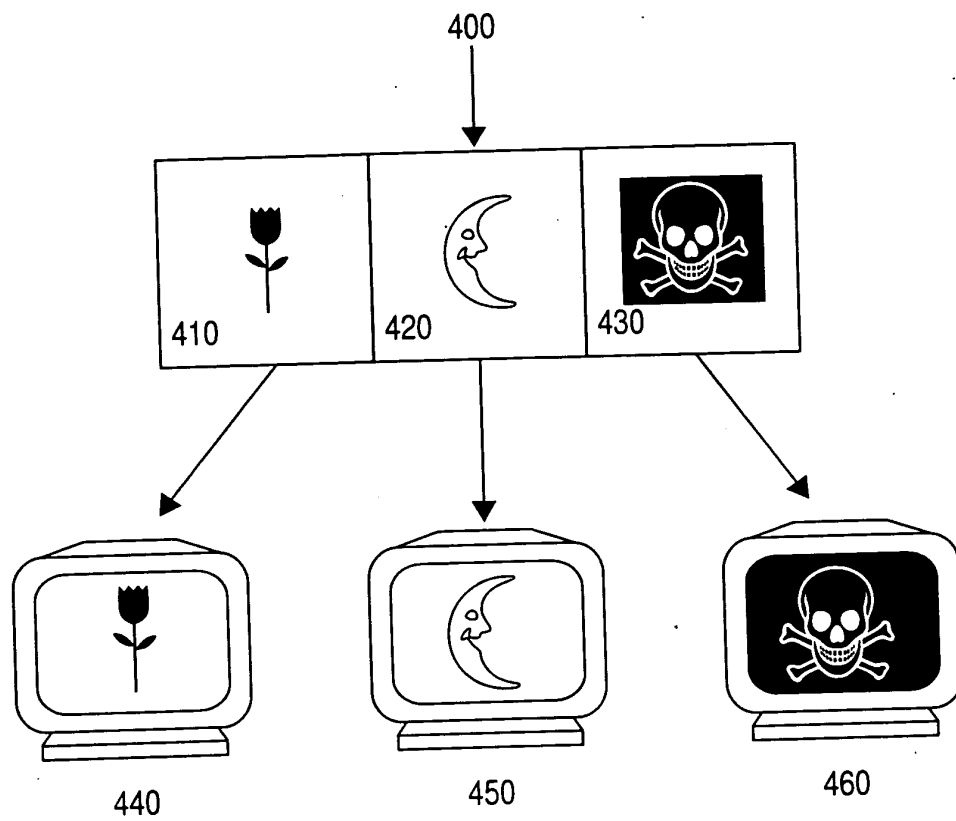


FIGURE 4

09733579.040201

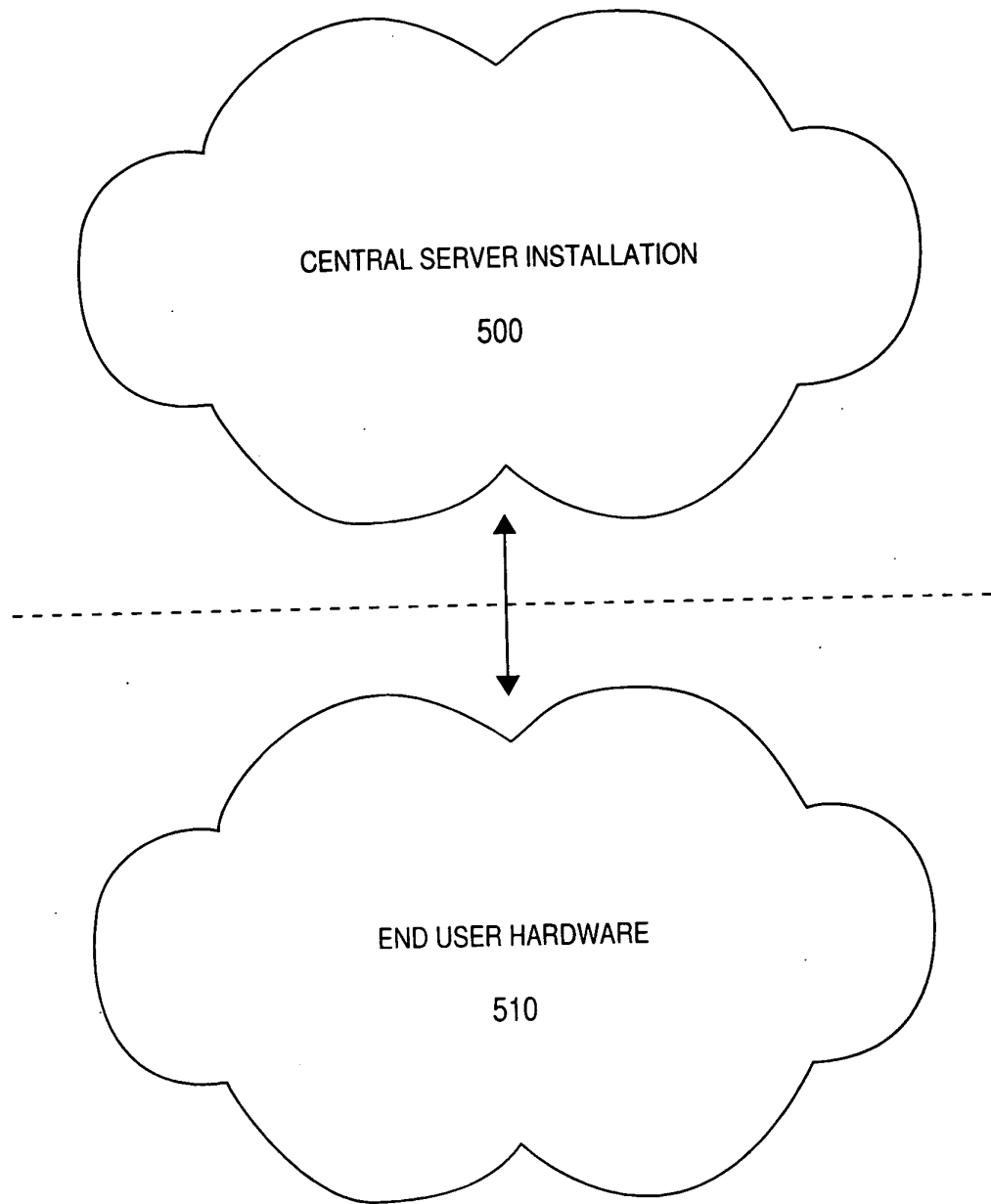


FIGURE 5

FIG. 6

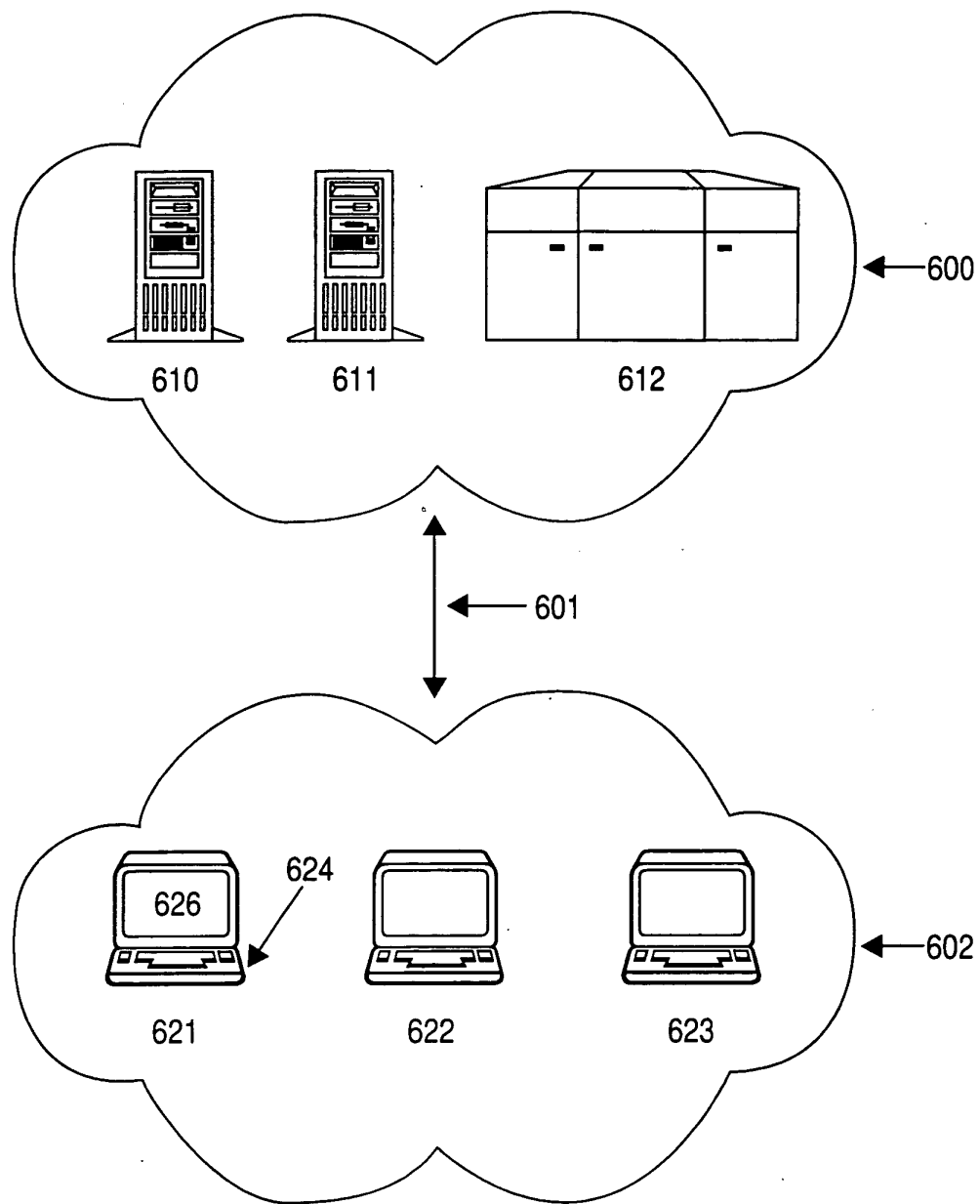


FIGURE 6

The diagram illustrates a system architecture with a central **712 PCI BUS** connecting various components. On the left, **705 FLASH MEMORY**, **704 EMBEDDED PROCESSOR**, and **706 DRAM** are stacked vertically. The **704 EMBEDDED PROCESSOR** is connected to the **712 PCI BUS**. To the right of the bus are **708 SMARTCARD INTERFACE** and **709 VIDEO CONTROLLER**. The **709 VIDEO CONTROLLER** is connected to the **712 PCI BUS** and also to **710 VIDEO DECODER** and **711 VIDEO ENCODER**. At the bottom, **701 USB CONTROLLER**, **703 AUDIO CODEC**, and **702 NETWORK CONTROL BLOCK** are connected to the **712 PCI BUS**. External connections are shown: **713** for the **701 USB CONTROLLER**, **716** for the **703 AUDIO CODEC**, **714** for the **702 NETWORK CONTROL BLOCK**, and **715** for the **709 VIDEO CONTROLLER**. The label **HID** is positioned near the **713** connection point.

FIGURE 7

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T02040-625EE260

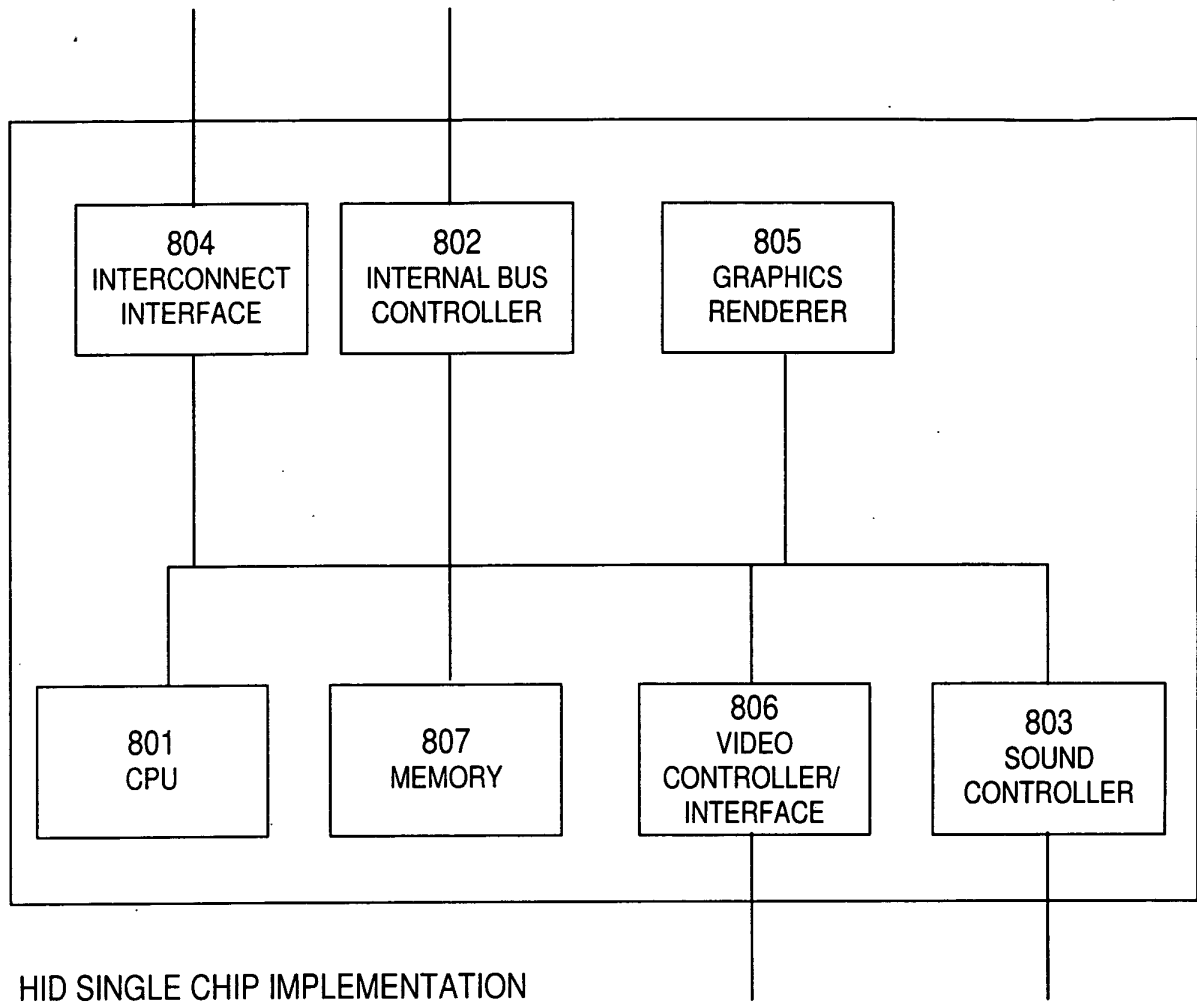


FIGURE 8

09733579-04001
T.02040" 645EE260

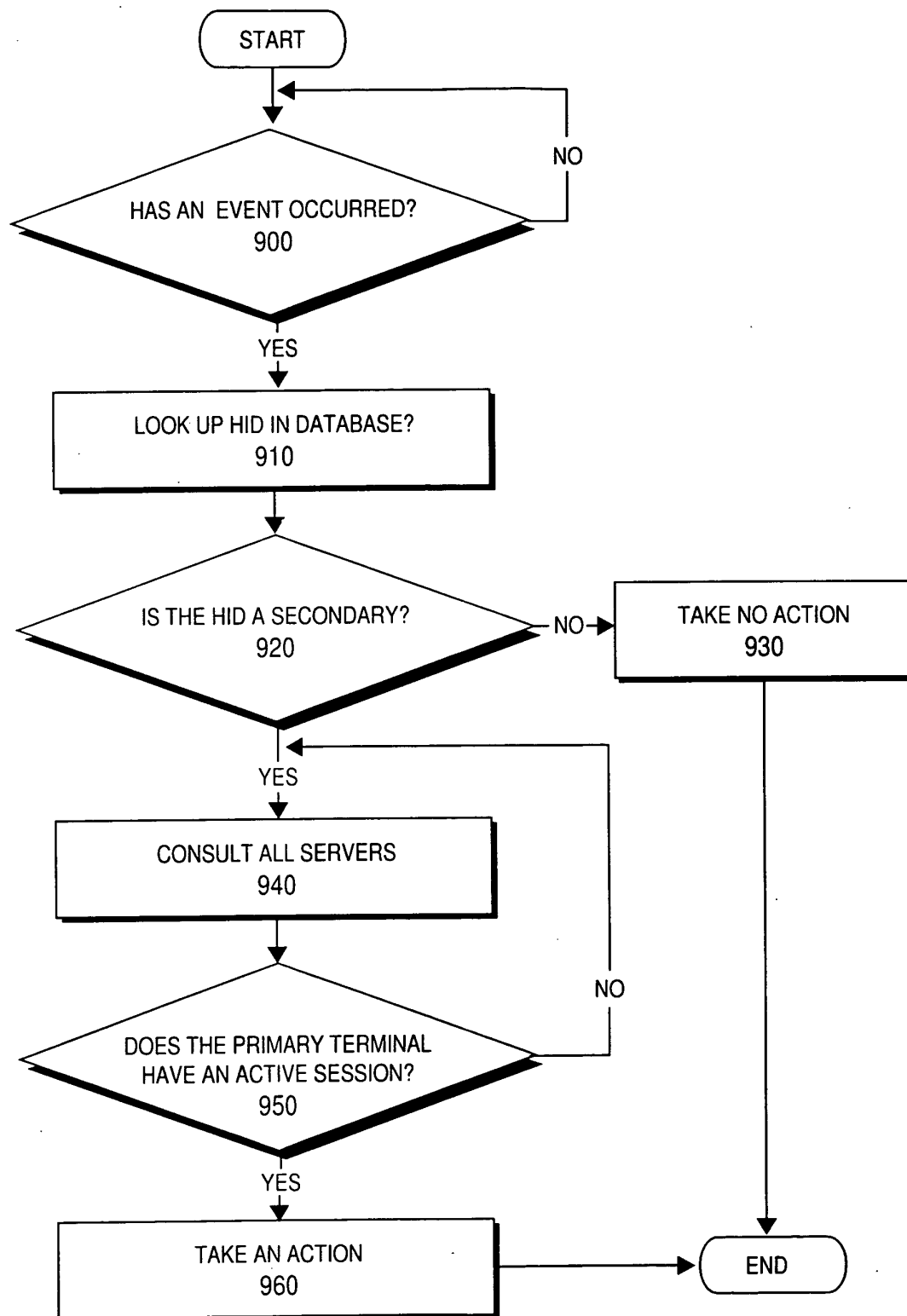


FIGURE 9

09733579-040201

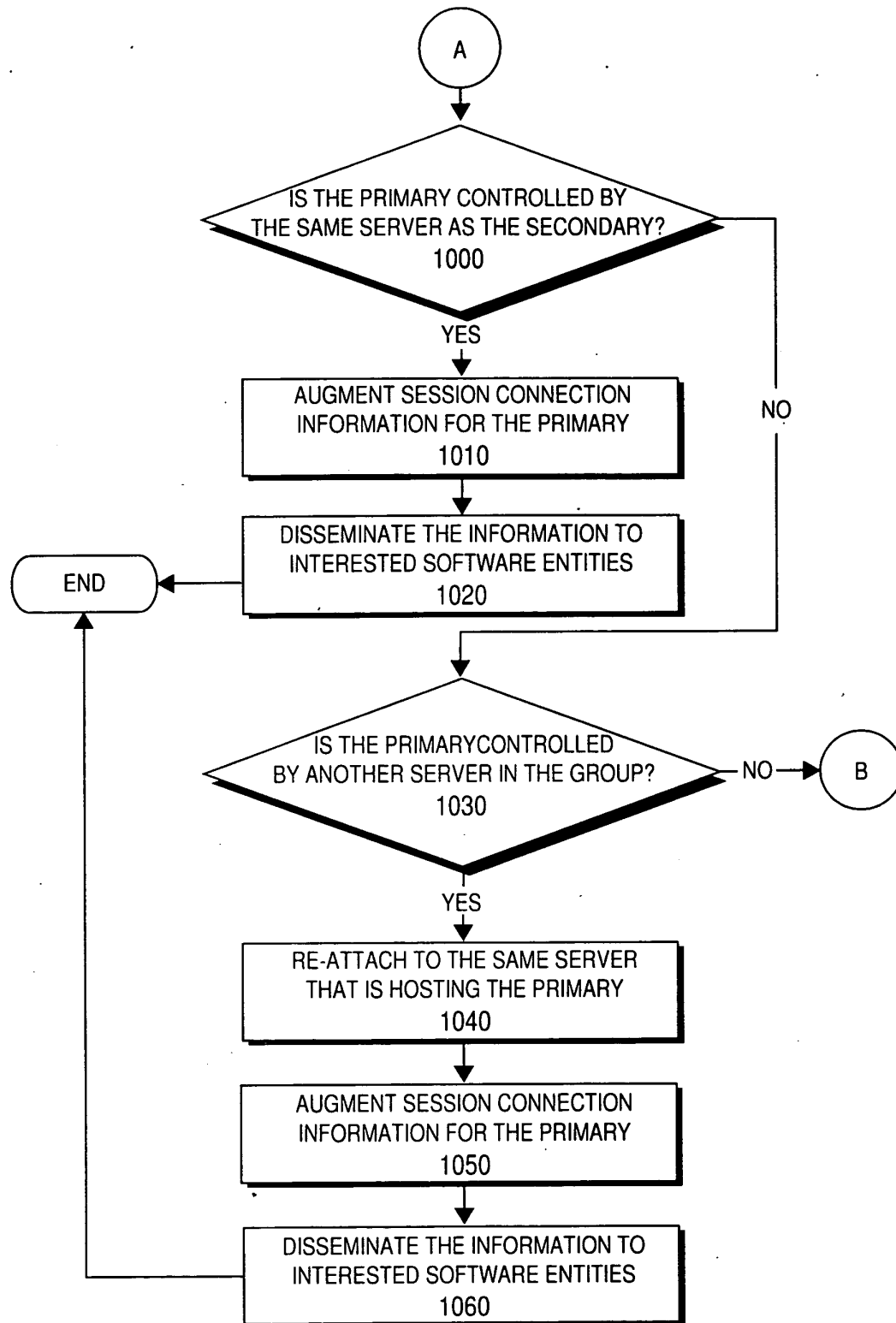


FIGURE 10A

09733579.040201
T02010 6255260

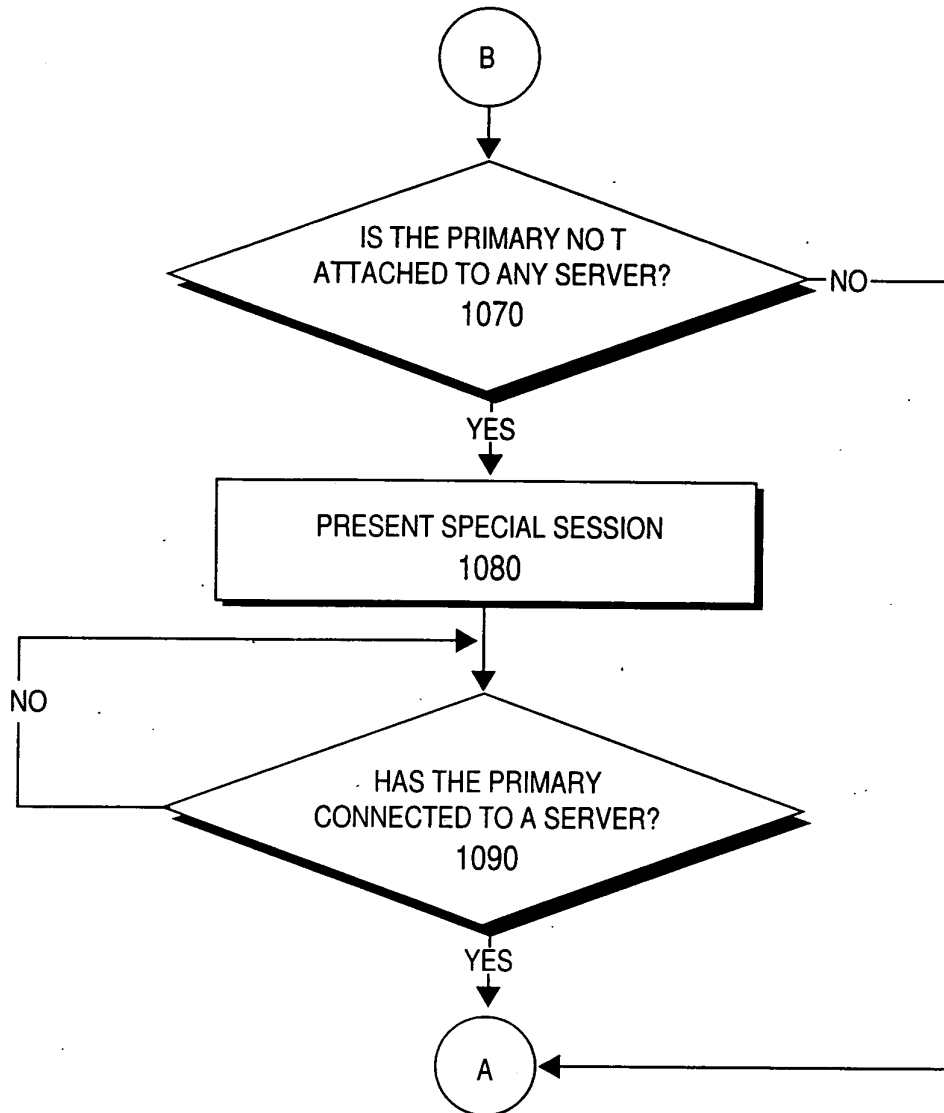


FIGURE 10B

09733579-040201

